## Push-Pull Balance W T Cocking

reprint

Wireless World November, 1947.

One of the more important reasons for adopting push-pull amplification, especially in an output stage, lies in the fact that under ideal conditions all even-order harmonics, introduced by curvature of the valve characteristics, are balanced out and do not appear in the output. In addition to this, the mean anode currents of the valves act in opposition in the output transformer and the core consequently works under better conditions. Because of this transformer distortion is also reduced. It is important to notice that the push-pull connection only reduces

even-order harmonics. Consequently, its use is of value only if the distortion introduced by the valves is predominantly of this kind. This is so with triodes and for long no other valves were used in push-pull. When operated under optimum conditions for a single output valve, a pentode or tetrode usually introduces more third than second harmonic. The use of a pair of such valves in push-pull then gives relatively little advantage over a pair in parallel, but there is still some advantage, largely because of the improved conditions in the output transformer brought about by the removal of the direct polarizing current. The distortion introduced by a tetrode or pentode depends very much upon the magnitude of its load resistance, however. With a low value

of load the distortion is predominantly second harmonic, just as with a triode, and this distortion tends to decrease as the load is increased, again just as with a triode. Unlike the triode, however, third harmonic distortion grows rapidly as the load resistance is increased. Although small when this resistance is small it greatly exceeds the second harmonic when it becomes high. When pentodes are used in push-pull, therefore, it usually pays to work with an effective load on each which is much less than the optimum for the non push-pull case. The third-harmonic distortion is reduced and the second-harmonic distortion is balanced out by the

push-pull connection. While there is no doubt at all about the value of push-pull in both theory and practice, many people realize that the ideal conditions which are usually assumed in theoretical discussions do not exist in practice. They realize, for instance, that the two valves of a push-pull stage are never exactly alike, and they wonder how much the performance is affected by this. Then, again, they know that various balancing schemes are sometimes recommended and they wonder if they are necessary when other amplifiers perform satisfactorily without them. It is not easy to find answers to questions such as these and very little definite information on the subject has been published. The simplest method of attacking the problem is to express the characteristics of the valves by power series, but even if only a few

terms are taken the expressions become very cumbersome, and it is not easy to take the load impedance into account. The equations in the Appendix show the second and third-harmonic distortion produced by valves having characteristics which can be represented by cubic equations, ignoring the effect of the load. The distortion for a pair of valves in either parallel or push-pull is given. The signal voltages are represented by e1 for one valve and e2 + e3, for the other; e1 and e2 are in opposite phase for push-pull and e3 is

in phase quadrature. This last term is included so that the case when

the input voltages to the two valves are not exactly in opposite phase

are respectively  $\mu 1$  and  $\mu 2$  times the actual input voltages E1, E2 and

can be considered; e1 on the one hand and e2 and e3, on the other

First of all, e3 will be taken as zero. It can be seen from the equations in the Appendix that both the fundamental and third- harmonic components are the same whether the valves are in parallel or in push-pull. With the latter circuit both the out-of-balance direct current in the output transformer and the second-harmonic distortion can be reduced to zero by the adjustment of the relative values of v1, v2, e1 and e2. If the two valves are exactly alike the requirement for perfect balance is v1 = v2 and e1 = e2 that is, the two valves must have the same grid bias and their inputs must have the same amplitude, and be in

given by Equation (4) by writing minus signs instead of + between the pairs of terms in brackets. Because of this a favourite way of balancing a push-pull amplifier is to insert a resistance

shunted by a pair of phones in the HT lead, as shown dotted in Fig. 1, and to adjust the relative values of the input voltages E1 and E2 for zero fundamental tone in the phones. Of course, in this particular case, since the output stage is assumed itself to be perfectly balanced, all that is done is to equalize E1 and E2 and so to correct for imperfections in the preceding stage. Phase Unbalance The effect of E3 is to produce output currents in quadrature to those caused by E1 and E2, but through interaction between them it does also produce currents additive to the main ones. Because of the

unbalance can often be tolerated.

variations of components and valves.

In this case the fundamental frequency

current in the HT lead is also zero. It is

E3.

opposite phase.

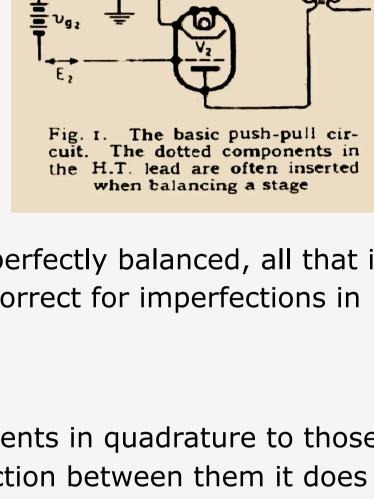


Figure 1

harmonic distortion to zero and equality of mean anode currents cannot be maintained at all signal levels. However, these effects are very small in practice because it is not difficult to keep the phase errors small. It is by no means easy to make them zero but in a common type of phase-splitter they can be

avoided, for their correct use demands some degree of skill. It is, therefore, the usual commercial practice to dispense with them. This is done by determining a permissible degree of unbalance which affects the performance negligibly, and then designing the equipment so that this degree of unbalance is not exceeded with any normal

Under such conditions neither the out-of-balance current nor the second-harmonic distortion will usually be zero, but this does not matter provided that the resulting distortion is small enough. An output stage might, for instance, be designed for 2% third-harmonic distortion at full output. The second-harmonic distortion might vary from the ideal zero to, perhaps, 1% according to the degree of

of second- than of third-harmonic distortion. It is, therefore, almost

certain that the extra distortion would not be aurally detectable.

However, this may be, it is certain that there is some degree of second-harmonic distortion relative to the third harmonic which cannot be detected in any listening test. Provided that the second harmonic can be kept below this figure, therefore, its presence is immaterial, and if an amplifier can be designed which is inherently well enough balanced to keep the distortion below this figure its performance is as good as one more perfectly balanced. It is also much more satisfactory in practice. In order to determine how much unbalance is permissible in an amplifier it is necessary to know how much second-harmonic distortion can be allowed. This can only be settled by elaborate listening tests with large numbers of observers. For present purposes it will be assumed that an amount of second harmonic equal to the third harmonic is unimportant. This is a 3db increase in distortion from an 'ideal' level of -34db, assuming that 2% third harmonic is the target. The distortion level thus rises from the 'ideal' -34db to -31db and this in itself would not be readily detected by ear. When the greater tolerance of the ear to second-harmonic distortion is taken into account; it seems certain that its presence in this degree is unimportant. At present this must be largely a matter of opinion, and it is possible to work out the permissible unbalances for any desired ratio of

but it is necessary to work out the permissible unbalances far each case. The best that can be done, therefore, is to pick a typical case and the following values will be assumed:  $b1 = 2 \times 10$ -4,  $c1 = -3 \times 10$ -7, d1=  $1.25 \times 10$ -9, e1 = 120V, v1 = 180V. These values correspond to a fictitious valve having 300V HT, -40V grid bias,  $\mu = 3$  and a signal input of 40V peak. A single valve, or a pair in parallel, produce 2% third-harmonic distortion and 10% second harmonic. It is required to determine how the second-harmonic distortion of a pair in push-pull varies with the various out-of-balance elements. This is done by taking the above values for the valves and Figure 2

these valve constants the second harmonic does not exceed 0.4 of the third. The effect of varying v2/v1 is shown in curve D. This, however, is not a grid-bias variation but one of total voltage. It can be converted to one of grid-bias variation in this instance by multiplying the 'Percentage Unbalance' scale in Fig. 1 by 1.5. By using equation (13) the effect of varying e3/e2 can be computed and is shown in curve E. In a practical case these variations may all occur together but it is clear from Fig. 2 that in order of importance the various causes of unbalance are e3/e2, v2/v1, e2/e1, c2/c1, d2/d1. This is rather a fortunate order, for the valve variations are of least importance and it is these which are least under the control of the amplifier designer. It is well to allow a maximum variation here and it is not unreasonable to take c2/c1 = d2/d1 = 1.2; that is, 20% unbalance between valves.

If the valves are operated in the usual way with a common bias

silicon-steel core is used.

0.03.

permissible input unbalance so determined. The result is shown by the curve DISTORTION of Fig. 3 and it will be seen that Figure 3 for unity distortion ratio an input unbalance of as much as 13% HARMONIC can be allowed. This is for the worst case when the input 티 unbalance is in the same sense

case, in practice, however, for if the two input voltages are dissimilar then one valve will run into grid current earlier than the other unless the valve with the larger input has a proportionately higher grid bias. Very serious distortion usually occurs when grid current starts, and if one valve draws grid current before the other the amplifier is overloading before the other valve is giving its full output. Conditions in this respect are likely to be at their worst when an amplifier carefully balanced for zero second-harmonic and out-ofbalance currents! The poorer valve of the two may need the greater input and the smaller grid bias for balance. The tolerance permissible on E1/E2 is thus likely to be tighter for the

appearance of excessive second-harmonic distortion. This is not the

seem desirable. It is suggested that an input tolerance of  $\pm 5$  percent should be aimed at in designing the preceding stages and that there should be not more than 3% phase unbalance at high and low frequencies, This permits rather greater tolerances on valves than have been considered in the foregoing. No attempt at balancing second-harmonic distortion to zero should normally be made and it is only necessary to balance the mean anode currents in exceptional cases or when high-permeability core material

APPENDIX.

Let the characteristics of valves 1 and 2 be expressed by

is used in the output transformer.

avoidance of marked disparities in the grid-current overload points of

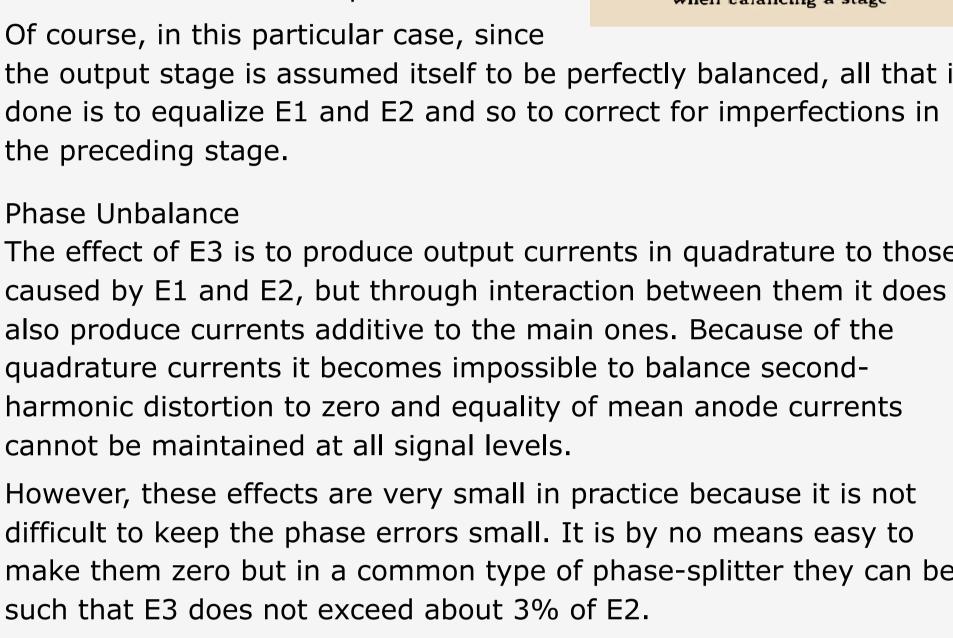
the two valves than for avoidance of second-harmonic distortion. If

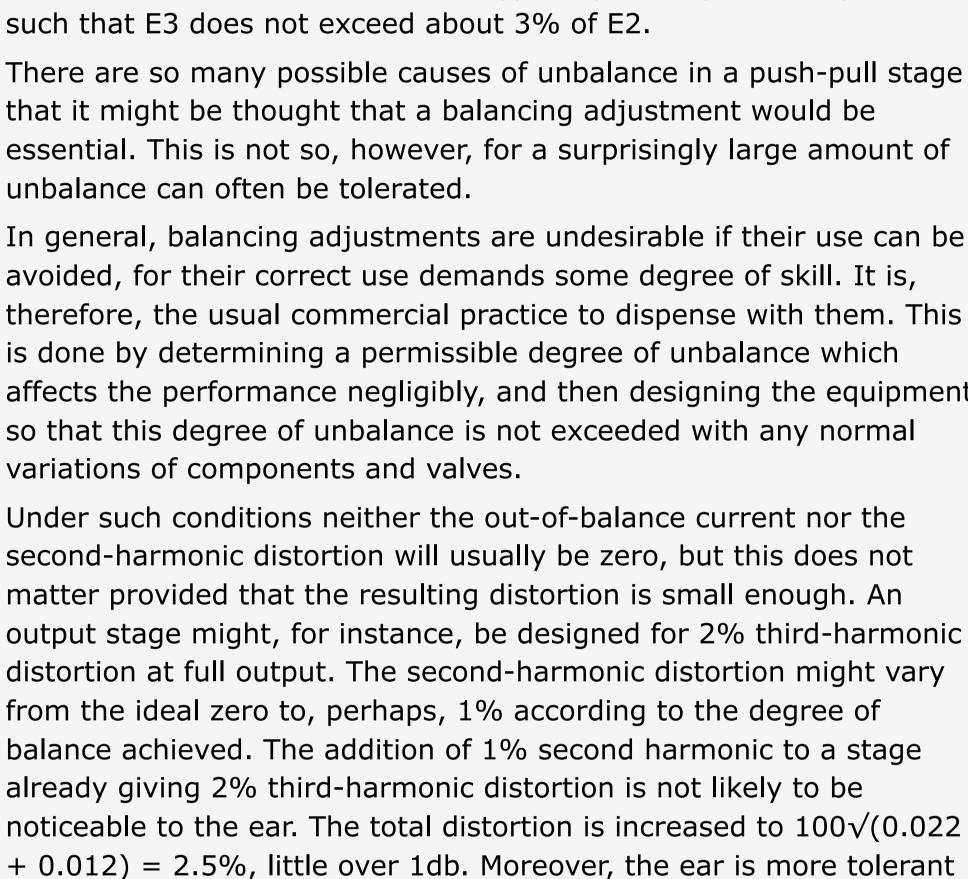
the bias is 40V and the maximum input is 40V peak, then with the

 $i_{a1} = a_1 + b_1 V_1 + c_1 V^2 + d_1 V_1^3$  $i_{a2} = a_2 + b_2 V_2 + c_2 V_2^2 + d_2 V_2^3$  ... ... ... where  $V_1 = V_{a1} + \mu_1 V_{\sigma 1} + \mu_1 E_1 \sin \omega t = v_1 + e_1 \sin \omega t$   $V_2 = V_{a2} + \mu_2 V_{\sigma 2} - \mu_2 E_2 \sin \omega t - \mu_2 E_3 \cos \omega t = v_2 - e_2 \sin \omega t - e_3 \cos \omega t$ When the values are combined in push-pull the effective current is  $i_{ai} = i_{aa}$ whereas when they are in parallel it is  $i_{a1} + i_{a2}$  and  $V_2 = v_2 + e_2 \sin at$ . Inserting the values of  $V_1$  and  $V_2$  in (1) and (2) and expanding, we get: Out-of-balance D.C. component =  $I_{DC}$  =  $(a_1 - a_2) + (b_1v_1 - b_2v_2) + (c_1v_1^2 - c_2v_2^2) + (d_1v_1^3 - d_2v_2^3)$  $+\frac{1}{2}(c_1e_1^2-c_2e_2^2)+\frac{3}{2}(d_1e_1^2v_1-d_2e_2^2v_2)-\frac{1}{2}c_2e_3^2-\frac{3}{2}e_3^2v_2d_2$ Fundamental frequency component =  $I_1$  =  $\sin \omega t [(b_1e_1 + b_2e_2) + 2(c_1e_1v_1 + c_2e_2v_2) + 3(d_1e_1v_1^2 + d_2e_2v_2^2)]$  $+\frac{3}{4}(d_1e_1^3+d_2e_2^3)+\frac{3}{4}e_3^2e_2d_3$  $+\cos \omega t [b_1 e_3 + 2c_2 e_3 v_2 + 3e_3 v_2^2 d_2 + \frac{3}{4} d_2 e_2^2 e_3 + \frac{3}{4} d_2 e_3^3]$ Second-harmonic component =  $I_2$  =  $-\cos 2\omega t \left[ \frac{1}{2} (c_1 e_1^2 - c_2 e_2^2) + \frac{3}{2} (d_1 e_1^2 v_1 - d_2 e_2^2 v_2) + \frac{1}{2} c_2 c_3^2 + \frac{7}{2} d_2 e_3^2 v_2 \right]$  $+\sin 2\omega t[-c_2e_2e_3-3d_2e_2e_3v_2+3d_2e_2e_3^2]$  ..... Third-harmonic component =  $I_3$  =  $-\sin 3\omega t [(d_1e_1^3+d_2e_2^3)]$ +  $\cos 3\omega t \left[ \frac{1}{4} d_2 e_3^3 + \frac{3}{4} d_2 e_2^2 e_3 + \frac{3}{4} d_2 e_2 e_3^2 \right] \dots$ When  $e_0 = 0$ , for two values in parallel or push-pull, the fractional third

harmonic distortion is  $\frac{1}{4}(d_1e_1^3+d_2e_2^3)$  $(b_1e_1+b_2e_2)+2(c_1e_1v_1+c_2e_2v_2)+3(d_1e_1v_1^2+d_2e_2v_2^2)+\frac{3}{4}(d_1e_1^3+d_2e_3^3)$ parallel, - signs-for push-pull)  $\frac{1}{2}\left[\left(c_{1}e_{1}^{2}+c_{2}e_{2}^{2}\right)+3\left(d_{1}e_{1}^{2}v_{1}+d_{2}e_{2}^{2}v_{2}\right)\right]$  $(b_1e_1 + b_2e_2) + 2(c_1e_1v_1 + c_2e_2v_2) + 3(d_1e_1v_1^2 + d_2'e_2v_2^2) + \frac{3}{4}(d_1e_1^3 + d_2e_2^3)$ Therefore, the ratio of 2nd/3rd harmonic distortion is  $\frac{1}{2\frac{c_1}{d_1c_1}} \cdot \frac{\left(1 \pm \frac{c_2}{c_1} \cdot \frac{e_2^2}{e_1^2}\right) + 3\frac{d_1v_1}{c_1}\left(1 \pm \frac{d_2}{d_1} \cdot \frac{e_2^2}{e_1^2} \cdot \frac{v_2}{v_1}\right)}{1 + \frac{d_2}{d_1} \cdot \frac{e_2^3}{e_1^3}}$ 

.. (7) When  $e_3 = 0$ , the fractional second harmonic is (+ signs for two valves in .. (8) For zero second harmonic distortion the requirement is For zero out of balance current it is necessary to have :---





2nd/3rd harmonic distortion. In the following examples, however, a figure of unity for this ratio is taken as the maximum permissible. For the present assume that there is no phase unbalance (e3 = 0). Then Equation (9) in the Appendix gives the ratio of 2nd/3rd harmonic distortion. The ratio does not depend only on the ratios e2/e1, v2/v1, c2/c1, d2/d1, but also on c1/d1, e1 and v1. It is not, therefore, possible to derive a general solution applicable to all valves,

HARMONIC DISTORTION calculating the 2nd/3rd harmonic ratios with the aid of Equation (9). The results are 22 22 shown in Fig. 2; curve A shows RATIO how the harmonic ratio varies as e2/e1 varies for the case when PERCENTAGE UNBALANCE AND PERCENTAGE E 1/E, the valves are identical and Fig. 3. The effect of varying  $e_2/e_1$  is shown here for the case when  $v_2/v_1=1$ v1/v2. For unity distortion ratio  $c_2/c_1 = d_2/d_1 = 1.2$ , and  $c_3/c_2 = 0.03$ . Curve A  $c_2/c_1 = d_2/d_1 = \nu_2/\nu_1 = 1$ ,  $e_1 = 0$ ,  $e_2/e_1$  varying. over 20% unbalance of the input C  $c_2/e_1 = c_2/c_1 = v_2/v_1 = 1$ ,  $e_3 = 0$ ,  $d_2/d_1$  varying. D  $e_2/e_1 = c_2/c_1 = d_1/d_1 = 1$ ,  $e_3 = 0$ ,  $v_2/v_1$  varying. E  $e_2/e_1 = c_2/c_1 = d_2/d_1 - v_2/v_1 = 1$ ,  $e_3/e_2$  varying. voltages is permissible. Curve B shows the effect, with e2 = e1, d2 = d1 of varying c2/c1 and curve C illustrates the effect of varying d2/d1 with c2 = c1. Even with 20% difference between either of

resistor v2/v1 = 1. The out-of-balance current in the output transformer rarely exceeds 10mA, in practice, and this causes negligible distortion when a well designed transformer having a The phase unbalance between the inputs is usually negligible over the major part of the audio-frequency spectrum. At very low and very high frequencies it becomes of appreciable magnitude but with good design it is not large. It occurs chiefly in the phase-splitter and may amount to about 3%. The value of e3/e2 will, therefore, be taken as With these values as reasonable tolerances to allow for variations in values and for some phase unbalance the ratio of 2nd to 3rd harmonic distortion for various values of e2/e1 can now be calculated and the

RATIO as the others; when it is PERCENTAGE UNBALANCE IN E./E2 opposing, a much bigger Fig. 3. The effect of varying  $e_2/e_1$  is shown here for the case when  $\nu_2/\nu_1 = 1$ ,  $c_2/c_1 = d_2/d_1 = 1.2$ , and  $e_3/e_2 = 0.03$ . tolerance is permissible. **Grid Current Effects** It will be clear from all this that a push-pull stage is very far from being critical and what at first appear to be quite large amounts of unbalance have quite a small effect on the performance. This is in accord with practical experience. In the foregoing it has been assumed that there is no limit to the permissible value of E1/E2 (e1/e2) other than that set by the

13% tolerance found earlier, the input to the other valve will be only 35.4V. As the power output is proportional to the square of the input voltages it will be about (75.4/80)2 = 0.89 of that which would be obtained with equal input voltages and identical valves. The loss is not large, but a somewhat tighter input tolerance would